



Atty. Dkt. No. 016907-1577

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Hitoshi TSUNO

Title: SEMICONDUCTOR DEVICE INCLUDING MOS FIELD EFFECT TRANSISTOR  
HAVING OFFSET SPACERS OR GATE SIDEWALL FILMS ON EITHER SIDE  
OF GATE ELECTRODE AND METHOD OF MANUFACTURING THE SAME

Appl. No.: 10/676,264

Filing Date: 10/02/2003

Examiner: Unassigned

Art Unit: 2811

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §1.56**

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of a document known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56. A copy of each listed document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

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**TIMING OF THE DISCLOSURE**

The listed document is being submitted in compliance with 37 CFR §1.97(b), before the mailing date of the first Office Action on the merits.

**RELEVANCE OF EACH DOCUMENT**

The relevance of the foreign-language document is described in the present specification. An English translation of the foreign-language document is not readily available. However, the absence of such translation does not relieve the PTO from its duty to consider the submitted foreign language document (37 CFR §1.98 and MPEP §609). An English translation of the Abstract, Summary and Figure captions are provided herewith.

Applicant respectfully requests that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date MAR 12 2004

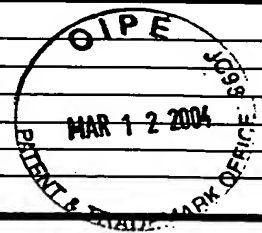
By *Richard L. Schwaab*

FOLEY & LARDNER LLP  
Customer Number: 22428  
Telephone: (202) 672-5414  
Facsimile: (202) 672-5399

Richard L. Schwaab  
Attorney for Applicant  
Registration No. 25,479

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Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: <b>MAR 12 2004</b> (use as many sheets as necessary)				<b>Complete if Known</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/676,264</td> </tr> <tr> <td>Filing Date</td> <td>10/02/2003</td> </tr> <tr> <td>First Named Inventor</td> <td>Hitoshi TSUNO</td> </tr> <tr> <td>Group Art Unit</td> <td>2811</td> </tr> <tr> <td>Examiner Name</td> <td>Unassigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>016907-1577</td> </tr> </table>		Application Number	10/676,264	Filing Date	10/02/2003	First Named Inventor	Hitoshi TSUNO	Group Art Unit	2811	Examiner Name	Unassigned	Attorney Docket Number	016907-1577
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U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
	B1	K. Ota et al., "Double Offset Implantation Technique for High Performance 80nm CMOSFET with Low Gate Leakage Current", SEMI Forum 2002, ULSI Technology Seminar, Section 4, pages 42-47.	

Examiner Signature	Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, PO Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.